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## FACSIMILE TRANSMITTAL SHEET

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| <b>Deliver To:</b>      | Examiner Carol S. Tsai – Art Unit 2857         |
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| <b>From:</b>            | Jason Graff                                    |
| <b>Date:</b>            | July 30, 2003                                  |
| <b>Time:</b>            |  |
| <b>Number of Pages:</b> | 5 (Including Cover Sheet)                      |
| <b>Operator:</b>        | Nadya Gordon                                   |
| <b>Our Reference:</b>   | Docket No. 042390.P6518                        |

|                 |  |
|-----------------|--|
| <b>SUBJECT:</b> | Serial No. 09/408,918  |
| <b>REMARKS:</b> | Per our conversation July 30, 2003, included is the "Intel Invention Disclosure Form" you requested. |

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## INTEL INVENTION DISCLOSURE

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Legal ID # **10155** (Legal use only)

DATE: 8/5/98

It is **important** to provide accurate and detailed information on this form (fill in ALL areas under Inventor(s)). The information will be used to evaluate your invention for possible filing as a patent application. When completed, please return this form to the Legal Department at HF3-03. If you have any questions regarding this form or to whom it should be forwarded, please call 756-1369 or 696-2851 or 554-3996. **PLEASE, PRINT CLEARLY OR TYPE.**

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(PROVIDE SAME INFORMATION AS ABOVE FOR EACH ADDITIONAL INVENTOR)

2. Title of Invention: A Methodology to extend the I/O Architecture System Management Interface to allow a host processor to Gather and Modify performance monitor data collected by an I/O processor.
3. Stage of Development, i.e. % complete, and relation of technology to the following product/process: 95% complete, will be shipped on the Pro I/O Software Developer's kit in September, 1998.
4. (a) Has a description of your invention been, or will be shortly be, published?  
 NO ☐ YES ☒ DATE WAS OR WILL BE PUBLISHED ~~September 1998~~ **January 1999**  
 If YES, was the manuscript submitted for pre-publication approval? NO ☒ YES ☐  
 (b) Has your invention been used/sold or planned to be used/sold by Intel or others?  
 NO ☐ YES ☒ DATE WAS OR WILL BE SOLD ~~September 1998~~ **January 1999**
5. If invention conceived, or constructed during performance of a government or third party contract, please check here and give full contract Name and Number: \_\_\_\_\_
6. Please attach a page to this form, DATED AND SIGNED BY ONE INVENTOR (PREPARER), to provide a summary of your invention and include the following information in your abstract.  
 (a) State general purpose(s) of your invention;  
 (b) Describe advantage(s) of your invention over what is done now;  
 (c) Describe essential elements(s) or key to your invention; and

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(d) Value of your invention of Intel (how will it be used?).

\*HAVE YOUR SUPERVISOR READ AND SIGN COMPLETED FORM.

DATE: 8/6/98 SUPERVISOR: Susan C. Krommald

DATE: 8/6/98 SUPERVISOR: Rose

BY THIS SIGNING, I (SUPERVISOR) ACKNOWLEDGE THAT I HAVE READ AND UNDERSTAND THIS DISCLOSURE, AND RECOMMEND THAT THE HONORARIUM BE PAID.

## INTEL INVENTION DISCLOSURE

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## A Methodology to extend the I<sub>2</sub>O Architecture System Management Interface to allow a host processor to Gather and Modify performance monitor data collected by an I/O processor.

Susan C. Kromenaker, Mark L. Brown, Linda M. Roberts, William C. Arthur, Jr.  
8/6/98

### Purpose

This invention provides a method for a host processor to gather and modify data collected by an I/O processor containing a Performance Monitor unit. This invention extends the I<sub>2</sub>O Architecture by implementing private parameters on the IOP that can be extracted from the I/O Processor by the host, via the I<sub>2</sub>O Architecture System Management Interface.

### Advantages *Host processor*

The I<sub>2</sub>O Architecture provides a method for gathering data from an I/O processor called the System Management Interface. Data is stored on the I/O Processor and extracted by the host processor by sending an *UtilParamsGet* message to the I/O Processor. The host is able to modify data stored on the I/O Processor by sending an *UtilParamsSet* message. Embedded within the messages is the exact data to modify and retrieve. This data is stored on the I/O Processor in parameter groups. For more information on the I<sub>2</sub>O Architecture, please review the *Intelligent I/O (I<sub>2</sub>O) Architecture Specification*, revision 1.5.

The newest Intel I/O processor, the 80960 RM and 80960 RN, contains a performance monitor unit. This unit gathers a variety of data that can be used for performance analysis, or for real-time system tuning. For more information, please review the *Tuzigoot/Huachuca Microprocessor EAS (Internal)*, revision 2.0. A separate patent disclosure has been filed for the performance monitor unit.

The I<sub>2</sub>O Architecture does not provide a method for configuring, or controlling the I/O Processor's performance monitor hardware, nor for gathering data from the performance monitor hardware. This invention extends the I<sub>2</sub>O Architecture System Management interface and provides this capability.

By extending an industry standard such as the I<sub>2</sub>O Architecture to provide a method for gathering and modifying performance monitor data, Intel is providing a standard method of configuring and controlling performance monitor hardware on an I/O Processor. Intel is also providing a standard method of gathering performance monitor data from the I/O Processor. A software vendor developing tools to tune a server need only implement this method of configuring and controlling the Performance monitor hardware, and only implement this method of gathering performance monitor data from any I/O Processor that implements a performance monitor unit. No longer does the software vendor need to tweak their tools for each I/O processor.

A hardware vendor that implements a product utilizing an I/O Processor with a Performance Monitor Unit need only implement this method of letting the host control and configure the performance monitor hardware and gather performance monitor data. They are guaranteed to have their product work under any Operating System that implements the I<sub>2</sub>O Architecture, including NT, NetWare, SCO UNIX, HP-UX, OS/2 and Solaris. No OS changes are required. This significantly cuts down on development and maintenance costs.

### Key Elements

A Private Class called Performance Monitor has been defined. The methodology for defining a Private I/O Class is defined in the Intelligent I/O (I<sub>2</sub>O) Architecture Specification.

New parameter groups have been defined that contain the performance monitor data and performance monitor setup information. Software running on the host can modify parameters stored in these groups via the *UtilParamsSet* message. The type of information contained in these parameters is the information necessary for the I/O Processor to control and configure the Performance Monitor Hardware.

Software running on the host can gather performance monitor data in these new groups via the *UtilParamsGet* message. The type of data that is typically read from the parameters groups is the control and configuration information status, as well as the actual performance monitor results.

The exact parameter groups and their contents are defined in the *Performance Monitor Software API*, revision 1.1, which is attached to this document.

**INTEL CONFIDENTIAL****Intel Uses**

CPD has already documented and implemented this invention. We plan to ship the associated software running on the I/O Processor in a Software Developer's Kit in September, 1998. We also plan to work with tool vendors to implement this methodology in their system performance monitoring tools.